## Jordan McGhee

20 4<sup>th</sup> Ave SE, Oelwein, IA 50662 (319) 238-9963 JMcGheeCPE@gmail.com

Dedicated and hardworking hardware engineer with an interest in computer architecture and RTL design. Large range of experiences ranging from aircraft design, network protocols, and GPU architecture. Looking for a position where I will be able to solve tough problems and better myself as a hardware logic designer.

## PROFESSIONAL EXPERIENCE

Insurezone, Oelwein, IA
Tech Team Intern May 2018—Present

- Developed an Action Point parser for porting the existing web application to mobile
- Tested and Integrated new code into the existing codebase
- Helped plan and develop new features for the Insurezone platform

Oelwein High SchoolOelwein, IATech Team MemberAugust 2015–May 2018

- Repaired and maintained MacBooks for student and faculty use
- Provided I.T. Services for the members of the Oelwein Community School District
- Helped set up new tech infrastructure as the school district grew

## **EDUCATION**

IOWA STATE UNIVERSITY Ames, IA

Bachelor of Science in Computer Engineering August 2019-Present

IOWA STATE UNIVERSITY Ames, IA

Bachelor of Science in Aerospace Engineering

August 2019-Present

**PROJECTS** 

GPU Ames, IA

• Implementing the Graphics Pipeline on a Xilinx FPGA

January 2022-Present

• Developed hardware logic for triangle rasterization

MIPS Processor Ames, IA

- Created a single cycle design, and a 5 stage pipelined design

  August 2021-January 2022
- Worked on debugging and verification framework (written in python)
- Used Cadence Synthesise, Route and Place, and Optimization tools

Digital Potentiometer

Created a digital 60k potentiometer, complete with custom Op-Amp
 January 2021-May 2021

Ames, IA

Greated a digital ook potentionieter, complete with custom op-Amp

MTCP Network Protocol

Ames, IA

• Created an implementation of the MTCP network protocol using Linux August 2021-January 2022

• Up to four simultaneous TCP Connections

## **ADDITIONAL SKILLS**

- Programming Languages: C/C++, Java, Python, VHDL, Verilog, VBScript, HTML/CSS/JavaScript, MIPS Assembly
- Course work: CPU/GPU Architecture, Digital/VLSI Design, Algorithms, A.I, Coding
- Tools: Genus, Innovus, Virtuoso, Quartus Prime, Vivado, Modelsim